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				<b>APPLICANT</b> <b>TAKAYAMA, SHUICHI, et al.</b>		
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<b>U.S. PATENT DOCUMENTS</b>						
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code(s) (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	US					
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<b>FOREIGN PATENT DOCUMENTS</b>						
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes-Number +-Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation
						Yes
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<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>						
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				
TTV		EDA, Hiroki. "Intel and HP Unveiled Outline of Their Next Instruction Set 'IA-64'" Nikkei Electronics, Nikkei Business Publications, Inc. November 3, 1997 (No. 702), page 27-28.				
TTV		KUMAGAI, Masayasu. "Details of VLIW and IA-64, and Comparisons with Alpha and PowerPC" Interface, CQ Publishing Co., Ltd. March 1, 1998, Vol. 24, No. 3, pp.132-139.				
EXAMINER <u>TED T. VO</u>			DATE CONSIDERED <u>11/23/04</u>			

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